MRI Brain Image Enhancement Using XILINX System Generator and DWT

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Abstract--- This paper presents a novel method of enhancing the image quality of human brain obtained from Magnetic Resonance Imaging scans. Here, we are trying to enhance the brain image using Xilinx System Generator, which is a DSP tool devised by Xilinx Corporation. The architecture presented in this paper utilizes a graphical user interface that bonds together MATLAB Simulink and XSG offering appropriate hardware implementation. Hence, the quality of image is improved. These images are, then, fed to artificial neural network, for normality or abnormality classification of brain. This paper utilizes ANN using multiperceptron and back propagation method. This method first makes use of discrete wavelet transform to extract energy values from enhanced images using filters and then it is fed to ANN for further classification. The image enhancement using XSG, has its performance measured in terms of resource utilization by using FPGA XC5VLX50T is also measured.

Keywords--- Artificial Neural Network (ANN), Field Programmable Gate Array (FPGA), Image Enhancement, Magnetic Resonance Imaging (MRI), Xilinx System Generator (XSG)

I. INTRODUCTION

DIGITAL image processing is expanding in all the spheres of our life. It may be defense, aerospace, biomedical application, biometric field, space exploration. The management and usage of such digitally processed image is growing progressively. With a raise in the demand for better visual content, the technology needs to be renewed and modernized.

Image processing in digital means is required for proper analysis, inference and based on that apt decision making. Hence, the image content and quality should be improvised. In this paper, we try to stress on processing the image in terms of pixels and work out an algorithm for these pixels present in the image and reconstitute the image, back from the pixel values.

In this paper, we are trying to enhance the quality and Visual content of MRI of the brain using Xilinx System and classifying further for the brain image is normal or abnormal. Quality of image can be made better in many ways. Here, we are just trying to enhance the image for better perception and visual content. Also, we are providing hardware implementation on FPGA Virtex 5 making use of Xilinx system Generator. XSG is a versatile DSP tool developed by Xilinx Corporation.

Magnetic Resonance Imaging (MRI) is a medical imaging technique which produces high quality resolution images of anatomical parts like brain, chest etc. This technique is used in areas like biomedical field and provides critical information for clinical diagnosis and research.

FPGA is beneficial over DSP or ASIC based hardware implementation as it has higher reprogram ability, parallel computational ability. FPGA also provides a straightforward approach while implementing it by using Verilog Hardware Descriptive Language.

In this paper, we aim to implement an algorithm for image enhancement on brain MRI image using system generator. We start our process with implementing our architecture for enhancement using blocks available in Simulink reservoir of MATLAB R2012a and .m files. Using ISE 13.4 tool from Xilinx, which is compatible with System generator, we obtain a bitstream code. Also again by using Xilinx simulator we can simulate the results on modelsim. Using Chipscope pro tool, we dump the bitstream file on FPGA for hardware The design and implementation on FPGA hardware is done using MATLAB based Simulink blocks in XSG prototype.

II. LITERATURE SURVEY

An extensive research has been performed by the researchers on image enhancement and classification based on ANN. In the method proposed by S. Allin Christe et al., [1] uses edge detection technique. Many image filtering algorithms have been proposed and tumor characterization is done using XSG, where they achieve resource utilization up to 50% for hardware implementation.

Kalyani Dakre and P.N Pusdekar [2] try to focus on the different image enhancement algorithms like contrast stretching, thresholding, negative transformation etc., to be implemented on FPGA using XSG. Zhang Shanshan and Wiang Zong [3] have proposed architecture for recognizing features of edges in a vehicle. The system implementation is using hardware Spartan 3e to get the resource utilization. In the paper presented by Pooja Gupta and Kuldip Pahwa [4] stress on how to improve the image visual quality using pixel reconstruction and Gabor filtering technique, the color enhancement showing its maximum improvement.

Arun Kumar Mittal and Sukhwinder Singh [5] explained brain tumor detection can be made, using histogram equalization and image processing techniques, which were used to segment the brain tumour region. In [6], the paper has
implemented different image enhancement techniques using spatial domain and frequency domain to enhance the visual quality and have tried to implement it on FPGA.

Image enhancement methods approach using Verilog Hardware Descriptive Language by Iuliana Chiuchisan et al., [7] focuses on image enhancement in spatial domain for the point processes algorithms like contrast stretching threshold operation, to provide easier approach to implement it on hardware. K. Anil kumar and Vijay Kumar [8] have presented various image processing operations using FPGA. They have also utilized Matlab Simulink blocks and XSG.

Sami Hasan et al., [9] have presented parallel 2D image filtering algorithm and its implementation on FPGA. All total of nine algorithms have been implemented using Xilinx System generator within 12.3 Design suite and verified the results using two virtex 6 FPGA board. Tarek M.Bitibissi et al., [10] have implemented five image processing algorithms in spatial domain like median filter, contrast stretching, Histogram equalization etc. and implemented it on FPGA Cyclon III under Altera systems.

A.C Suthar and et al., [11] have presented hardware software co-simulation which can be done in model based approach and about enhancement of the image. The paper presented by Menaka R and Rohini S [12], is dealing with detection of blockage of arteries in human brain by using watershed transform approach. Their neural network implementation has an efficiency of 90% and hardware implementation is done on Spartan 3E.

In [13], the authors Yousuf Khan and et al., have used edge detection on Vascular images. They have enhanced the images using simulink model. Ed-Edily et al., [14] have proposed an automatic tumor detection and localization in MRI images which can detect and locate the tumor using edge detection and segmentation methods.

Y. Zang et al., [15] have presented an automated and accurate classification of MR brain images using adaptive chaotic swarm optimization method. They have used Feed forward network to classify the image as normal or abnormal. This method first employs a wavelet transform to extract features from images, and then applies the technique of principle component analysis (PCA) to reduce the dimensions of features. The reduced features are sent to an FNN, and these parameters are optimized.

In [16], Alareqi Mohamed et al., have presented hardware software co-simulation for image processing algorithms using system generator. In [17], V. Elaraman and G.Rajkumar have presented the paper on Implementing point processes on FPGA. They have utilised system generator to provide hardware in the loop and cosimulation approach.

Aniket Ingle and Vrushali G. Raut [18] developed an algorithm for edge detection using Xilinx system generator and they have hardware implementation on Spartan 3e. Kavitkar S.G and Paikrao P.L [19] have implemented many edge detection algorithms such as Roberts edge detection, Sobel edge detection etc. using Xilinx system Generator. They have also provided FPGA implementation to the generated algorithms to provide hardware in the loop approach.

### III. PROPOSED MODEL

The main block diagram for implementation of the system is shown in Figure 1. The proposed block diagram for hardware implementation on FPGA using Xilinx system generator is shown in Figure 2.

![Fig. 1: Block Diagram of the System](image1)

![Fig. 2: Design Flow for Hardware Implementation](image2)

#### A. Image Pre-Processing Block

Any image processing will have pre-processing block present always. This is a common block which suppresses the unwanted noise present in the image required for further processing. The other name for image preprocessing is as filtration. Image preprocessing has been done here, to remove the noise present in the image such as speckle noise, foreground noise, background noise etc. present in the image. Due to preprocessing, the input to FPGA is in the form of test vector array which is suitable bit stream compilation using Xilinx for FPGA using the system generator. The image preprocessing blocks shown in Figure 3 contains the following:

- **Resize:** By setting up an input dimension for an image and bi-cubic interpolation, it helps in preserving finer details of an image.
- **Convert 2D to 1D:** converts two dimensional pixels present in an image to one dimensional or single dimensional array of pixel values.
- **Frame conversion and buffer:** helps in setting the sampling mode and buffer helps in storage of pixel values.
B. Image Enhancement Block

This is the most important block. Here, the image enhancement block, we are trying to increase the brightness and contrast of the brain MRI image. Here, we are using Gabor filtering and histogram equalization method to enhance the image. There is an automatic generation of Verilog code from MATLAB-Simulink blocks present in the algorithm. For image enhancement, we are trying to use two techniques:

1. Histogram Equalization

Histogram equalization is one of the techniques used in spatial domain. Due to the simplicity and performance on all kinds of images, histogram equalization is in wide use. Histogram equalization technique performs contrast stretching on the neighborhood pixels in the image. If there are m different pixel values such that m≥0 having L gray levels xᵢ where i=0, 1, 2 …L-1, with probability pᵢ(xᵢ), then probability pᵢ(xᵢ) can be calculated from histogram h(xᵢ), the number of pixels with value xᵢ

\[ pᵢ(xᵢ) = h(xᵢ)/Σ_{i=0}^{L-1} h(xᵢ) \quad (1) \]

2. Gabor Filtering

The frequency and orientation representation of Gabor filter in spatial domain is similar to that of the human eye. The Gabor filter is a Gaussian function modulated by sinusoidal wave. The Gabor filter is given in the Equation (2)

\[ h(x,y)=g(x,y) * s(x,y) \quad (2) \]

where g(x,y) is Gaussian function ;
\[ s(x,y) \] is sinusoidal function.

By the convolution property of the Gabor filter, the convolution of Fourier transforms of Gaussian and Harmonic function gives the impulse response of Gabor function.

C. Image Post Processing

Image post processing is the converse of the image pre-processing block and plays a different role. Image post processing helps in recreation of the image from single array of pixels i.e. displaying back the whole image. The image post processing block shown in Figure 5 contains:

- Data conversion type: This block converts the image into unsigned integer UINT format which is of 8 bits.
- Buffer: It is a storage device; used for storing the pixel values. It also converts frames to samples at a lower conversion rate.
- Convert 1D to 2D: converts single array of pixels to two dimensional array image matrix.
- Video Viewer: It displays the output image on the screen.

D. Energy Feature Extraction using DWT

Once we get the enhanced image, we try extracting energy feature values using DWT, to get the image in all the four directions i.e. 0, 90, 180, 270 degrees. For this, we are dealing with following types of filters:

1. Daubechies filter- 2 to 24.
2. Biorthogonal filter- 3.2 to 7.9.
3. Symlets filter- 2.2 to 23.

The above DWT filters are characterized by number of vanishing moments. As the vanishing value increases, the sharpness of the curve increases. The DWT is applied for two levels.

1. Bi-orthogonal Filter

This filter may be orthogonal or non orthogonal. This is an invertible filter. It can be used to generate symmetric wavelet function. In this paper, we are using biorthogonal filter -3.3, 3.5, 3.7.
2. Symlets Filter
They are modified version of Daubechies wavelets with more symmetricity. They range from 2 to 23 in which we are using sym3.

3. Daubechies Filter
Its an orthogonal wavelet having more number of vanishing moments. We are using Daubechies filter 3.

E. Artificial Neural Network Classification
The artificial neural network is used to classify the normality or abnormality of the brain. The abnormality of brain image is classified using technique called multilayer perceptron using back propagation technique.

A multilayer perceptron (MLP) is a feed-forward artificial neural network model that maps sets of input data onto a set of appropriate outputs. A MLP consists of multiple layers of nodes in a directed graph, with each layer fully connected to the next one. Except for the input nodes, each node is a neuron (or processing element) with a nonlinear activation function.

MLP utilizes a technique called called-back-propagation for training the network. Back-propagation or "backward propagation of errors", is a common method of training artificial neural networks used in conjunction with an optimization method such as gradient descent. Back-propagation requires a known, desired output for each input value in order to calculate the loss function gradient.

IV. Algorithm

![Algorithm for Image Enhancement](image)

Fig. 6: Algorithm for Image Enhancement
The algorithm is shown in Figure 6. First using Simulink, we implement our architecture for enhancement which is a block model in MATLAB R2012a with an extension .m files. Using ISE 13.4 tool from Xilinx, which is compatible with System generator, we obtain a bit stream code. Also again by using Xilinx simulator we can simulate the results on Modelsim simulator. Using Chipscope pro tool, we dump the bit stream file on FPGA for hardware. The design and implementation on FPGA hardware is done using MATLAB based Simulink blocks in XSG prototype.

V. PERFORMANCE ANALYSIS
The design and implemented image enhancement algorithm for the MRI image is shown in Figure 7.

![Image Enhancement Algorithm](image)

Fig. 7: Image Enhancement Algorithm
The RTL schematic view for the proposed image enhancement algorithm is shown in Figure 8.

![RTL Schematic View](image)

Fig. 8: RTL Schematic View
The enhanced image output for a given MR brain image is shown in Figure 9 and Figure 10.

![Input Image](image)
![Output Image](image)

Fig. 9: Input Image  
Output Image
The mapping of pixel values on the output as generated in the simulation results by Xilinx ISE 13.4 is shown in Figure 11 for the brain image shown in Figure 9.

The hardware implementation on FPGA Virtex 5 is shown in Figure 12. We are using XILINX ISE 13.4 and Matlab R2012a. The enhanced image is given to artificial neural network to extract energy feature values using DWT filters for Figure 8 is shown in Figure 13, 14, 15, 16, 17.

Table 1: Resource Utilization in Hardware Implementation

<table>
<thead>
<tr>
<th>RESOURCES</th>
<th>AVAILABLE</th>
<th>USED</th>
<th>PROPOSED METHOD RESOURCES UTILISATION (%)</th>
<th>RESOURCE UTILISATION BY CHRISTE ET AL (%)</th>
<th>RESOURCE UTILISATION BY ZHANG (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>FLIP FLOPS</td>
<td>28800</td>
<td>87</td>
<td>1</td>
<td>2</td>
<td>4</td>
</tr>
<tr>
<td>SLICES</td>
<td>28800</td>
<td>88</td>
<td>1</td>
<td>2.5</td>
<td>5</td>
</tr>
<tr>
<td>LUTS</td>
<td>28800</td>
<td>120</td>
<td>1</td>
<td>1.5</td>
<td>3</td>
</tr>
</tbody>
</table>
Fig. 16: Biorthogonal Type 3.5 Filter Output

Fig. 17: Biorthogonal Filter 3.7 Output

Based on the obtained energy values from the filters the brain image can be declared as normal or abnormal.

VI. CONCLUSION

Xilinx System Generator is a new versatile tool built to perform image processing in hardware and software. It provides rapid means to do hardware implementation of complex techniques used for processing images with minimum resource and minimum delay. Also, XSG provides parallelism, robustness and automatic area minimization. The need of rapid prototyping tools such as MATLAB Simulink and Xilinx System Generator are increasingly important in recent times because of time-to constraints. It also provides simplicity and easier to implement on Hardware. The enhancement of MRI image can be further be helpful to the surgeons to operate on, at accurate locations of the brain. The early detection of the disease related to brain can be considered as the future scope of the work.

REFERENCES


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